

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NISHIZAWA, Hirotaka et al.

Art Unit: 2887

Appln. No.: 10/550,578

Examiner:  
Labaze, Edwyn

Filed: October 20, 2005

Confirmation No.: 1323

For: SEMICONDUCTOR DEVICE

\* \* \*

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the document listed on the attached Form PTO-1449 is hereby cited.

The listed document is the publication of commonly assigned Application No. 10/550,278. The Examiner is requested to consult the image file wrapper of the `278 Application for consideration of any Office Actions as well as the claims currently pending therein.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10436) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this

paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

Date: January 08, 2009

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